```
Name            Front Panel;
Partno          TMD8003;
Revision        01;
Date            8/20/2006;
Designer        Len Bayles;
Company         TMD Innovations;
Location        None;
Assembly        None;
Device          g16v8a;

/*****************************************************************/
/*                                                               */
/*      Front Panle Logic                                        */
/*                                                               */
/*****************************************************************/

/*
 * Inputs:
 */

Pin 1 =  SWDP;
Pin 2 =  SWLL;
Pin 3 =  SWLH;
Pin 4 =  BUST;
Pin 5 =  WRST;
Pin 6 =  PINT;
Pin 7 =  MER;
Pin 8 =  STOP;

/*
 * Outputs:
 */

Pin 12 = EXWR;
Pin 13 = EXLH;
Pin 14 = EXLL;
Pin 15 = STOBE;
Pin 16 = RUN;
Pin 17 = DSI;
Pin 18 = BBST;


/*
 * Logic:
 */


EXWR = SWDP & WRST & STOP;
EXLH = SWLL & WRST & STOP;
EXLL = SWLH & WRST & STOP;
STOBE = (SWDP # SWLL # SWLH) & STOP;
RUN = !STOP;
DSI = !((BUST & STOP) # (PINT & !MER));
BBST = BUST & STOP;
```