



| Memory Configuration | | | | RTC Enable | |
|----------------------|-----|-----|-----|------------|---------|
| SW2 | SW1 | ROM | RAM | SW3 | RTC |
| Off | Off | 0k | 16k | Off | Disable |
| Off | On | 2k | 14k | On | Enable |
| On | Off | 4k | 12k | | |
| On | On | 8k | 8k | | |

| | | |
|----------------------------|----------------------|--------|
| TMD Innovations LLC | | |
| Memory | | |
| Len Bayles | Rev 1.0 8/13/2006 | Page 3 |